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EWm  
12-31-02

Docket No.: 042390.P10572

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Blaise Fanning

Application No.: 09/822,643

Filed: March 30, 2001

For: CONTROLLING CACHE MEMORY  
IN EXTERNAL CHIPSET USING  
PROCESSOR

Examiner: Gary J. Portka

Art Group: 2187

**AMENDMENT AND RESPONSE TO THE OFFICE ACTION**

Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, DC 20231-9998

Sir:

In response to the outstanding Office Action mailed September 17, 2002, please amend the above-identified Application as follows.

**IN THE DRAWINGS**

Please accept the accompanying Request for Approval of Drawing Changes.

**IN THE CLAIMS**

Following is a complete set of claims as amended with this response, which includes amendments to claims 1, 14, and 27.

**RECEIVED**

**DEC 30 2002**

**Technology Center 2100**